NATIONAL INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

M.Tech (ACS) Time Table (w.e.f. 22/08/2022)

Room No: MME 868 (OLD MME) **Academic Year 2022-23** **Semester : I**

	8-8:55	9-9:55	10-10:55	11-11:55	12-12:55		2-2:55	3-3:55	4-4:55
MON		**RFE**		**MFC**	**ADC**	**L**	**RFE Lab(DV)**		
TUE		**DET**	**ADC**	**EL-I**	**RFE**	**U**			
WED		**EL-II**	**DET**	**ADC**	**EL-I**	**N**	**DC Lab(KN)**		
THU			**EL-II**	**DET**	**MFC**	**C**	**RFE**		
FRI			**MFC**	**EL-I**	**EL-II**	**H**	**Seminar(SKLV)**		

DET	Detection and Estimation Theory	Sri. M.V. Raghunath
ADC	Advanced Digital Communication	Dr.Kalpana Naidu
RFE	RF Engineering	Dr.D.Vakula
MFC	Mathematical Foundation for Communication Engine	Dr.Ch.Rami Reddy
EL-I		
IOT&SN	IOT and Sensor Networks	Dr. S. K. L. V. Sai Prakash
ITC	Information Theory and Coding	Dr. A. Prakasa Rao
EL-II		
VLSID	VLSI System Design	Dr. Atul Kumar Nishad
DC lab	Digital Communication Lab	Dr. Kalpana Naidu
RFE Lab	RF Engineering Lab	Dr. D.Vakula
Seminar	Seminar	Dr. S. K. L. V. Sai Prakash

Dr.Md.Farukh Hashmi/ Dr. B.K.N.S Rao
Timetable I/C

Dr. P.Sreehari Rao
HOD

NATIONAL INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
M.Tech (EI&ES) Time Table (w.e.f. 22/08/2022)

Room No: ECE 315 **Academic Year 2022-23** **Semester : I**

	8-8:55	9-9:55	10-10:55	11-11:55	12-12:55		2-2:55	3-3:55	4-4:55
MON		**ADSD**	**ADSD**	**EL-I**	**ESD**	**L**			
TUE		**ESD**	**TSC**	**EL-II**		**U**	**DD/ES Lab(PM,PP)**		
WED		**VLSID**	**TSC**	**EL-I**	**EL-II**	**N**	**Seminar(MdFH)**		
THU		**EL-II**	**VLSID**	**ADSD**	**TSC**	**C**	**TSC Lab(KVS)**		
FRI			**EL-I**	**ESD**	**VLSID**	**H**			

TSC	Transducer and Signal Conditioning Circuits	Sri. K.V.Sridhar
ADSD	Advanced Digital System Design	Dr.P.Prithvi
ESD	Embedded System Design	Dr.P.Murlidhar
VLSID	VLSI System Design	Dr. Atul Kumar Nishad
EL-I		
FPGASD	FPGA System Design	Prof.T.KishoreKumar
EL-II		
MLA	Machine Learning and Applications	Dr.J.Ravi Kumar
TSC lab	Transducer and Signal Conditioning Circuits Lab	Sri. K.V.Sridhar
DD/ES lal	Digital Design and Embedded Systems Lab	Dr.P.Murlidhar,Dr.P.Prithvi
Seminar	Seminar	Dr. Mohamamd Farukh Hashmi

Dr.Md.Farukh Hashmi/ Dr. B.K.N.S Rao
Timetable I/C

Dr. P.Sreehari Rao
HOD

NATIONAL INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

M.Tech (VLSI) Time Table (w.e.f. 22/08/2022)

Room No: ECE302 **Academic Year 2022-23** **Semester :I**

	8-8:55	9-9:55	10-10:55	11-11:55	12-12:55		2-2:55	3-3:55	4-4:55
MON		**AICD**	**EL-I**	**EL-II**	**DM**	**L**	**DICD Lab(BL)**		
TUE		**EL-II**		**DICD**	**MCFT**	**U**	**Seminar(SK)**		
WED			**DM**	**AICD**	**EL-I**	**N**	**AICD Lab(PSR)**		
THU		**EL-I**	**DICD**	**EL-II**	**MCFT**	**C**			
FRI		**MCFT**	**DICD**	**DM**	**AICD**	**H**			

MCFT	Microchip Fabrication Techniques	Dr.M.Satish
DM	Device Modelling	Prof.N.Bheema Rao
DICD	Digital IC Design	Dr.B.Lakshmi
AICD	Analog IC Design	Dr. P.Sreehari Rao
EL-I		
FPGAD	FPGA Design	Dr. T V K Hanumantha Rao
NEMD	Nano-electronic materials and devices	Dr. V. Narender
EL-II		
VLSITT	VLSI Test and Testability	Sri.K.Sarangam
AICD lab	Analog IC Design lab	Dr. P.Sreehari Rao
DICD lab	Digital IC Design lab	Dr.B.Lakshmi
Seminar	Seminar	Sri.K.Sarangam

Dr.Md.Farukh Hashmi/ Dr. B.K.N.S Rao
Timetable I/C

Dr. P.Sreehari Rao
HOD