NATIONAL INSTITUTE OF TECHNOLOGY WARANGAL

(An Institute of National Importance)

WARANGAL – 506004, Telangana – INDIA

Department of Electronics and Communication Engineering

Time-Table (A.Y:2021-22, Semester: II)

Class: I M.Tech (EI&ES) **with effect from: 03-01-2022**

Course Coordinator: Dr Prakash Kodali

Time / Day	8 to 9 AM	10 to 11 AM	11.30 to 12.30 PM	12.30 to 1.30 PM	1.30 to 2.30 PM	3 to 4 PM
Mon	MLSIP	ACA/IOT	HW/SW	Lunch Break	Int.Instr	ERTOS
Tue	HW/SW		ERTOS		Seminar	
Wed	IAC	MLSIP			SOC/ IA lab	
Thu	ACA/IOT		ERTOS		IAC	Int.Instr
Fri		IAC	Int.Instr		Adv.Inst Lab	
Sat	Refer the Academic Calendar					

Sl. No	Sub Code	Subject	Name of the Faculty
1	EC5151	Intelligent Instrumentation (Int.Instr)	Sri. K V Sridhar
2	EC5152	Industrial automation and control (IAC)	Dr. Prakash Kodali
3	EC5153	Hardware/Software Co-design (HW/SW)	Dr.P. Muralidhar
4	EC 5163	Elective III: Embedded Real Time Operating System (ERTOS)	Prof.T. Kishore Kumar
5	EC 5166	Elective IV: Machine Learning for Speech and Image processing (MLSIP)	Dr.Md.Farukh Hashmi
6	EC 5171	Elective V: Advanced Computer Architecture (ACA)	Dr.M Satish
7	EC5154	Advanced Instrumentation Lab	Sri K V Sridhar
8	EC5155	SoC design and Industrial Automation Lab	Dr.P Muralidhar/ Dr.Prakash Kodali
9	EC5198	Seminar	Committee Coordinator: Dr Vasundhara

Dr.P. Sreehari Rao

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Department of Electronics and Communication Engineering

Time-Table (A.Y:2021-22, Semester: II)

Class: I M.Tech (VLSI) **with effect from: 03-01-2022**

Course Coordinator: Dr. Atul Kumar Nishad

Time / Day	8 to 9 AM	10 to 11 AM	11.30 to 12.30 PM	12.30 to 1.30 PM	1.30 to 2.30 PM	3 to 4 PM
Mon	MSD	MCA	PDA	Lunch Break	LPVLSI	ASIC
Tue	ASIC		PDA		VLSID	
Wed	MSD		PDA		MSD Lab	
Thu	MCA		VLSID		FPGA Lab/ PDA Lab	
Fri	LPVLSI		VLSID		Seminar	
Sat	Refer the Academic Calendar					

Sl. No	Sub Code	Subject	Name of the Faculty
1	EC5251	Physical Design Automation (PDA)	Prof.N.Bheemarao
2	EC5252	Mixed Signal Design (MSD)	Dr. P. Sreeharirao
3	EC5253	Low power VLSI Design (LPVLSI)	Sri. K. Sarangam
4	EC5260	**Elective III:** ASIC System Design (ASIC)	Dr.B.Lakshmi
5	EC5263	**Elective IV:** VLSI Design Verification(VLSID)	Dr.TVKH
6	EC5266	**Elective V:** Modern Computer Architecture (MCA)	Dr.M.Satish
7	EC5254	Mixed Signal Design Lab	Dr.P. Sreeharirao
8	EC5255	Physical Design Automation Lab	Prof.N.Bheemarao
9	EC5256	FPGA Design Lab	Dr. B. Lakshmi
10	EC5298	Seminar	Committee Coordinator: Dr Ekta Goel

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Department of Electronics and Communication Engineering

Time-Table (A.Y:2021-22, Semester: II)

Class: I M.Tech (ACS) **with effect from: 03-01-2022**

Course Coordinator: Dr. Gopiram

Time / Day	8 to 9 AM	10 to 11 AM	11.30 to 12.30 PM	12.30 to 1.30 PM	1.30 to 2.30 PM	3 to 4 PM
Mon	MLSIP/5G	ACN	AWC	Lunch Break	SA5G/MLNG	ERTOS/ON
Tue	M.N/Ws		ERTOS/ON		ACN	
Wed		MLSIP/5G			W.Comm Lab	
Thu	M.N/Ws	AWC	ERTOS/ON		SA5G/ MLNG	
Fri	**Seminar**		AWC		Networks Lab	
Sat	Refer the Academic Calendar					

Sl. No	Sub Code	Subject	Name of the Faculty
1	EC5351	Advanced Wireless Communication (AWC)	Dr.S. Anuradha
2	EC5352	Mobile Networks (M.N/Ws)	Prof.L.Anjaneyulu
3	EC5353	Advanced Communication Networks (ACN)	Sri S.K.L.V. Sai Prakash
4	EC5362	**Elective-III :** Optical Networks (ON)	Dr. Himansu Shekar Pradhan
	EC5163	**Elective-III :** Embedded Real Time Operating Systems (ERTOS)	Prof.T. Kishore Kumar
5	EC5364	**Elective-IV :** 5G communications (5G)	Sri M.V.Raghunadh
	EC5166	**Elective-IV :** Machine Learning for Speech and Image Processing (MLSIP)	Dr.Md.Farukh Hashmi
6	EC5368	**Elective-V :** Smart Antennas for 5G communications (SA5G)	Dr.A.Prakasarao
	EC5369	**Elective-V:** Machine Learning for Next Generation Communication Networks (MLNG)	Sri. K. Ravi Kishore
7	EC5354	Wireless Communication Lab	Dr. S. Anuradha
8	EC5355	Networks Lab	Prof. L. Anjaneyulu, Dr.Chayan
9	EC5398	Seminar	Committee Coordinator: Dr Amarjit Kumar

Dr.P. Sreehari Rao

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