NATIONAL INSTITUTE OF TECHNOLOGY WARANGAL

(An Institute of National Importance)

WARANGAL – 506004, Telangana – INDIA

Department of Electronics and Communication Engineering

Time-Table (A.Y:2020-21, Semester: I)

Class: I M.Tech (EI&ES) **with effect from: 29-09-2020**

Course Coordinator: Dr. P. Prithvi

Time / Day	8 to 9 AM	10 to 11 AM	11.30 to 12.30 PM	12.30 to 1.30 PM	1.30 to 2.30 PM	3 to 4 PM
Mon	OT	TSC	ESD	Lunch Break	DD/ES Lab	
Tue	ADSP	OT	TSC		Seminar	
Wed		ADSP	FPGA		TSC Lab	
Thu	FPGA	ADSD	ADSP		TSC	ESD
Fri	ESD	ADSD	ADSD		OT	FPGA
Sat	Refer the Academic Calendar					

Sl. No	Sub Code	Subject	Name of the Faculty
1	EC5101	Transducers and Signal Conditioning circuits (TSC)	Sri. K.V.Sridhar
2	EC5102	Advanced Digital System Design (ADSD)	Dr. P. Prithvi
3	EC5103	Embedded System Design (ESD)	Dr.P.Muralidhar
4	EC 5113	Elective -I : FPGA Design (FPGA)	Prof.T. Kishore Kumar
5	EC 5115	Elective-II : Optimization Techniques (OT)	Dr.J.Ravi Kumar
6	EC 5118	Elective-III : Advanced DSP (ADSP)	Prof.C.B.Rama Rao
7	EC5104	Transducers and Signal Conditioning Lab (TSC Lab)	Sri. K.V.Sridhar
8	EC5105	Digital Design and Embedded Systems Lab (DD/ES Lab)	Dr. P. Prithvi/ Dr.P.Muralidhar
9	EC5191	Seminar	Committee

Prof.L.Anjaneyulu

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Department of Electronics and Communication Engineering

Time-Table (A.Y:2020-21, Semester : I)

Class: I M.Tech (ACS) **with effect from: 29-09-2020**

Course Coordinator: Dr. S. Anuradha

Time / Day	8 to 9 AM	10 to 11 AM	11.30 to 12.30 PM	12.30 to 1.30 PM	1.30 to 2.30 PM	3 to 4 PM
Mon	WOC/ITC	ADC	DET	Lunch Break	DC Lab	
Tue	ADSP/OT	WOC/ITC	ADC			RFE
Wed	Maths	ADSP/OT	ADC		RFE Lab	
Thu	WOC/ITC	Maths	ADSP/OT		RFE	RFE
Fri	DET	DET	Maths		Seminar	
Sat	Refer the Academic Calendar					

Sl. No	Sub Code	Subject	Name of the Faculty
1	EC5301	Detection and Estimation Theory (DET)	Sri. M.V. Raghunadh
2	EC5302	Advanced Digital Communication (ADC)	Dr.V.V.Mani
3	EC5303	RF Engineering (RFE)	Dr.D.Vakula
4	EC5304	Mathematical Foundations for Comm. Eng (Maths)	Dr.Ch.Ram Reddy
5	EC5311 EC5313	Elective-I : I) Wireless optical communication (WOC) II) Information theory and coding (ITC)	Dr.Himansu Shekar Pradhan Dr.A.Prakasa Rao
6	EC5315 EC5316	Elective-II: I) Advanced DSP (ADSP) II) Optimization Techniques (OT)	Prof. C.B Rama Rao Dr.Gopi Ram
7	EC5305	Digital Communication Lab (DC Lab)	Dr.V.V.Mani
8	EC5306	RF Engineering Lab (RFE Lab)	Dr. D.Vakula
9	EC5391	Seminar	Committee

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Time-Table (A.Y:2020-21, Semester: I)

Class: I M.Tech (VLSI) **with effect from: 29-09-2020**

Course Coordinator: Dr. Atul Kumar Nishad

Time / Day	8 to 9 AM	10 to 11 AM	11.30 to 12.30 PM	12.30 to 1.30 PM	1.30 to 2.30 PM	3 to 4 PM
Mon	AICD	DICD	DICD	Lunch Break	AICD Lab	
Tue	NMD	AICD	DM		LPVD	DICD
Wed	MFT	NMD	DM		DICD Lab	
Thu		MFT	NMD		LPVD	DM
Fri	LPVD	AICD	MFT		Seminar	
Sat	Refer the Academic Calendar					

Sl. No	Sub Code	Subject	Name of the Faculty
1	EC5201	Microchip Fabrication Techniques (MFT)	Dr.M. Satish
2	EC5202	Device Modeling (DM)	Prof. N.Bheema Rao
3	EC5203	Digital IC Design (DICD)	Dr.B.Lakshmi
4	EC5204	Analog IC Design (AICD)	Dr.P.Sreehari Rao
5	EC5207	Elective –I: Nano-electronic Materials and Devices (NMD)	Dr.V.Narendar
6	EC5210	Elective –II: Low power VLSI Design(LPVD)	Dr.TVK Hanumantha Rao
7	EC5205	Analog IC Design Laboratory (AICD Lab)	Dr.P.Sreehari Rao
8	EC5206	Digital IC Design Laboratory (DICD Lab)	Dr.B.Lakshmi
9	EC5291	Seminar	Committee

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